

THAT WHICH IS CLAIMED IS:

1. A device for access to applications of a chip card comprising a microprocessor associated with an operating system working with a set of instructions, a program memory and a battery of applications in a
5 memory of the chip card, wherein the device comprises:

a register of the microprocessor to store a code, on several check bits, proper to an entity brought into play,

a call instruction and an instruction for the
10 return of the set of instructions to instantaneously and automatically update the register during the action by a new entity,

a checking device for the checking, as a function of the check bits, of the authorized nature of
15 the access to the zones of the memory of the chip card by the new entity that is called or takes action in the chip card,

a first link to transmit the check bits from the microprocessor to the checking device.

2. A device for access to applications of a chip card according to claim 1, comprising a second register to store a code proper to the applications active at the time of the last call instruction sent.

3. A device for access to applications of a chip card according to one of the claims 1 or 2, wherein the entity that is called or takes action in the chip card is an application of the battery of
5 applications.

4. A device for access to applications of a chip card according to one of the claims 1 or 2, wherein the entity is a hardware event.

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